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						Application / Conf. N .	Unknown 101719,743	
					LOSURE	Filing Date	November 21, 2003	
						First Named Invent r	Bernard J. New	
					LIOAIII	Art Unit	Windown 2816	
					ssary)	Examiner Name	Unknown HAI L. NOUYEN	
U	Sheet	1	of		1	Attorney Docket Number	X-1141 US	

OTHER NON PATENT LITERATURE DOCUMENTS									
Examiner Initials *	Cite No"	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T'						
_	- 1	XILINX INC., "Using Delay-Locked Loops in Spartan-II FPGAs," Application Note: Spartan II FPGAs, XAPP174, v1.1, January 24, 2000, pp 1-13, Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124	=						
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Exami Signat		HAT L. NGUYEN Date Considered 11/10/2004							

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